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CONFIRMATION NO. 6872

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**\*\* CONTINUING DATA \*\*\*\*\***

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**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2004-059633 03/03/2004  
 JAPAN 2004-059632 03/03/2004  
 JAPAN 2004-059634 03/03/2004  
 JAPAN 2004-362993 12/15/2004  
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**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***

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Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No				
Verified and Acknowledged	/JEREMY C NORRIS/ Examiner's Signature	Initials	JAPAN	47	31

**ADDRESS**

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 UNITED STATES

**TITLE**

Circuit board manufacturing method and circuit board

<input type="checkbox"/> All Fees
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**FILING FEE  
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